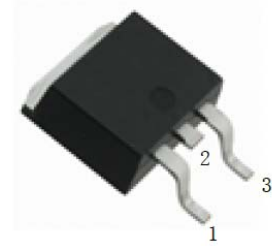
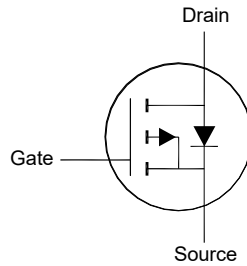


WTR03P080LS-HAF

P-Channel Enhancement Mode MOSFET

Features

- Halogen and Antimony Free(HAF), RoHS compliant



1.Gate 2.Drain 3.Source
TO-252 Plastic Package

Key Parameters

Parameter	Value	Unit
$-BV_{DSS}$	30	V
$R_{DS(ON)}$ Max	8.5 @ $-V_{GS} = 10$ V	m Ω
	12 @ $-V_{GS} = 4.5$ V	
$-V_{GS(th)}$ typ	1.5	V
Q_g typ	70 @ $-V_{GS} = 10$ V	nC

Absolute Maximum Ratings (at $T_a = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$-V_{DS}$	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current	$-I_D$	$T_c = 25^\circ\text{C}$	55
		$T_c = 100^\circ\text{C}$	34.6
Peak Drain Current ¹⁾	$-I_{DM}$	220	A
Avalanche Current	$-I_{AS}$	36	A
Avalanche Energy ²⁾	E_{AS}	65	mJ
Power Dissipation	P_D	43	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to + 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance - Junction to Case	$R_{\theta JC}$	2.9	$^\circ\text{C}/\text{W}$
Thermal Resistance - Junction to Ambient ³⁾ Steady State	$R_{\theta JA}$	38	$^\circ\text{C}/\text{W}$

¹⁾ Pulse Test: Pulse Width ≤ 100 μs , Duty Cycle $\leq 2\%$, Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)} = 150^\circ\text{C}$.

²⁾ Limited by $T_{J(MAX)}$, starting $T_J = 25^\circ\text{C}$, $L = 0.1$ mH, $R_g = 25$ Ω , $-I_{AS} = 36$ A, $V_{GS} = 10$ V.

³⁾ Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate in still air.

WTR03P080LS-HAF

Characteristics at $T_a = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at $-I_D = 250 \mu\text{A}$	$-V_{(BR)DSS}$	30	-	-	V
Drain-Source On-State Current at $-V_{DS} = 30 \text{ V}$	$-I_{DSS}$	-	-	1	μA
Gate-Source Leakage Current at $V_{GS} = \pm 20 \text{ V}$	I_{GSS}	-	-	± 100	nA
Gate-Source Threshold Voltage at $V_{DS} = V_{GS}$, $-I_D = 250 \mu\text{A}$	$-V_{GS(th)}$	1.2	-	2.5	V
Drain-Source On-State Resistance at $-V_{GS} = 10 \text{ V}$, $-I_D = 20 \text{ A}$ at $-V_{GS} = 4.5 \text{ V}$, $-I_D = 15 \text{ A}$	$R_{DS(ON)}$	-	7	8.5 12	$\text{m}\Omega$
DYNAMIC PARAMETERS					
Gate resistance at $V_{DS} = 0 \text{ V}$, $f = 1 \text{ MHz}$	R_g	-	2.2	-	Ω
Forward Transconductance at $-V_{DS} = 5 \text{ V}$, $-I_D = 10 \text{ A}$	g_{fs}	-	25	-	S
Input Capacitance at $V_{GS} = 0 \text{ V}$, $-V_{DS} = 15 \text{ V}$, $f = 1 \text{ MHz}$	C_{iss}	-	3636	-	pF
Output Capacitance at $V_{GS} = 0 \text{ V}$, $-V_{DS} = 15 \text{ V}$, $f = 1 \text{ MHz}$	C_{oss}	-	382	-	pF
Reverse Transfer Capacitance at $V_{GS} = 0 \text{ V}$, $-V_{DS} = 15 \text{ V}$, $f = 1 \text{ MHz}$	C_{rss}	-	306	-	pF
Total Gate Charge at $-V_{GS} = 10 \text{ V}$, $-V_{DS} = 15 \text{ V}$, $-I_D = 20 \text{ A}$ at $-V_{GS} = 4.5 \text{ V}$, $-V_{DS} = 15 \text{ V}$, $-I_D = 20 \text{ A}$	Q_g	-	70 33	-	nC
Gate-Source Charge at $-V_{GS} = 10 \text{ V}$, $-V_{DS} = 15 \text{ V}$, $-I_D = 20 \text{ A}$	Q_{gs}	-	12	-	nC
Gate-Drain Charge at $-V_{GS} = 10 \text{ V}$, $-V_{DS} = 15 \text{ V}$, $-I_D = 20 \text{ A}$	Q_{gd}	-	13	-	nC
Turn-On Delay Time at $-V_{GS} = 10 \text{ V}$, $-V_{DD} = 15 \text{ V}$, $-I_D = 20 \text{ A}$, $R_g = 3.3 \Omega$	$t_{d(on)}$	-	21	-	ns
Turn-On Rise Time at $-V_{GS} = 10 \text{ V}$, $-V_{DD} = 15 \text{ V}$, $-I_D = 20 \text{ A}$, $R_g = 3.3 \Omega$	t_r	-	59	-	ns
Turn-Off Delay Time at $-V_{GS} = 10 \text{ V}$, $-V_{DD} = 15 \text{ V}$, $-I_D = 20 \text{ A}$, $R_g = 3.3 \Omega$	$t_{d(off)}$	-	27	-	ns
Turn-Off Fall Time at $-V_{GS} = 10 \text{ V}$, $-V_{DD} = 15 \text{ V}$, $-I_D = 20 \text{ A}$, $R_g = 3.3 \Omega$	t_f	-	7	-	ns
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at $-I_s = 1 \text{ A}$, $V_{GS} = 0 \text{ V}$	$-V_{SD}$	-	-	1.2	V
Body-Diode Continuous Current	$-I_s$	-	-	55	A
Body-Diode Continuous Current, Pulsed	$-I_{SM}$	-	-	220	A
Body Diode Reverse Recovery Time at $-I_s = 20 \text{ A}$, $di/dt = 100 \text{ A} / \mu\text{s}$	t_{rr}	-	15	-	ns
Body Diode Reverse Recovery Charge at $-I_s = 20 \text{ A}$, $di/dt = 100 \text{ A} / \mu\text{s}$	Q_{rr}	-	9	-	nC

Electrical Characteristics Curves

Fig. 1 Typical Output Characteristic

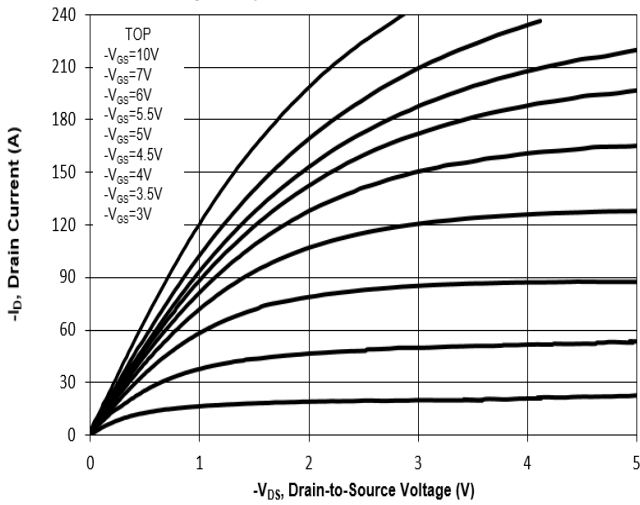


Fig. 2 Typical Transfer Characteristic

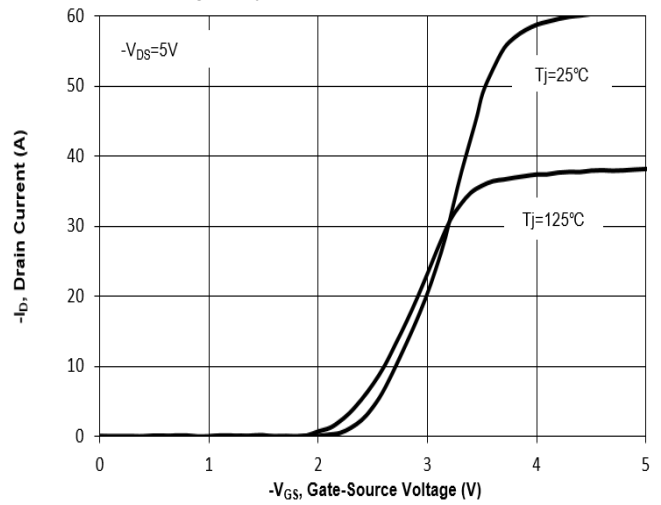


Fig. 3 On-Resistance vs. Drain Current

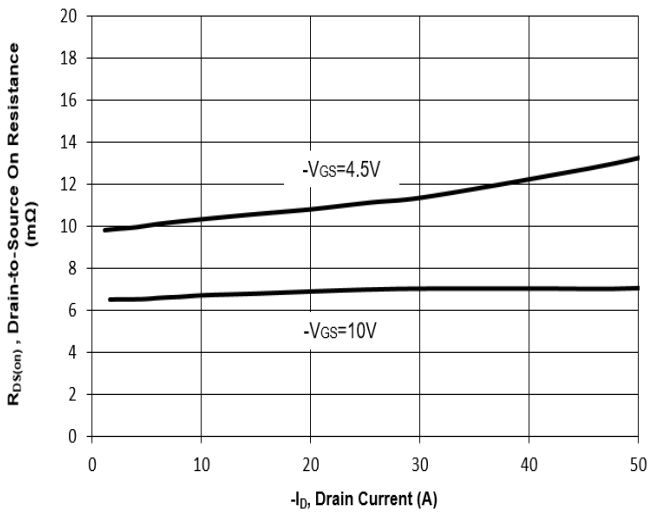


Fig. 4 On-Resistance vs. Gate Voltage

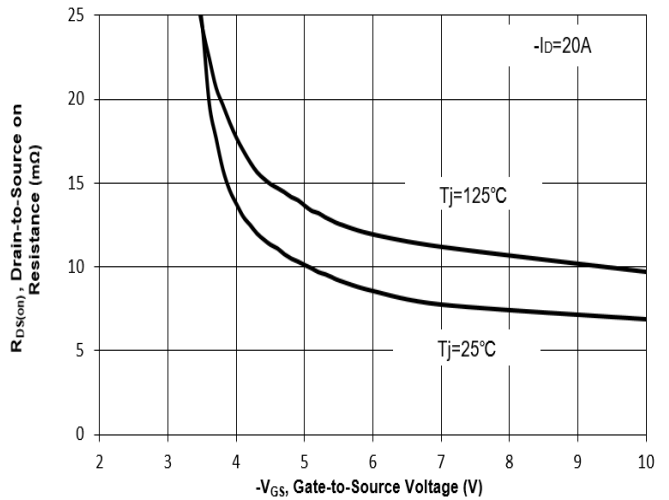


Fig. 5 On-Resistance vs. T_j

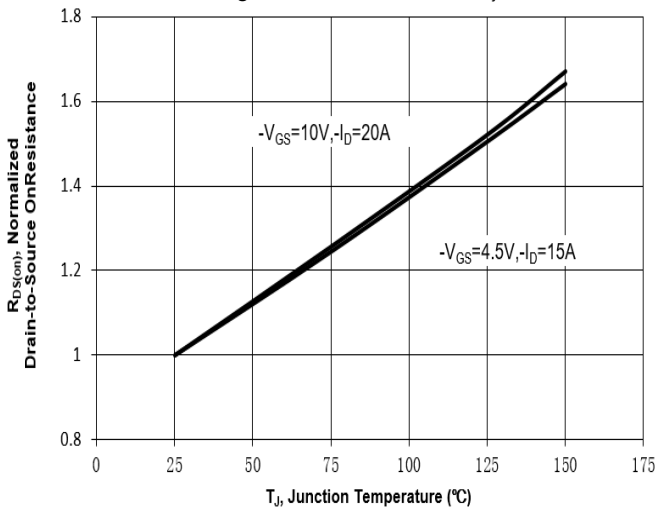
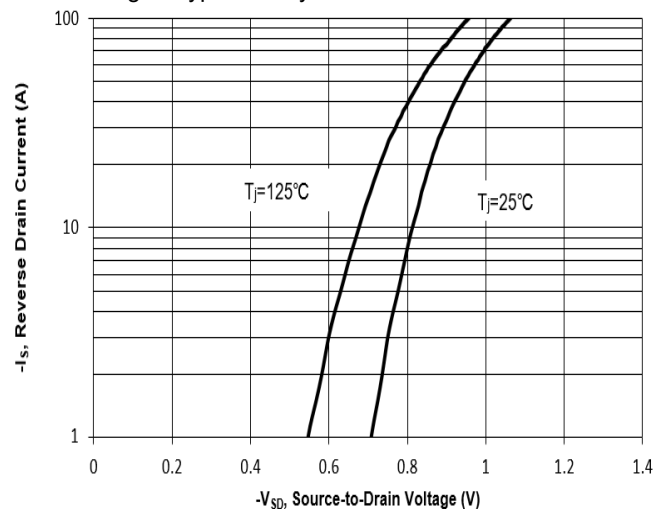


Fig. 6 Typical Body-Diode Forward Characteristic



Electrical Characteristics Curves

Fig. 7 Typical Junction Capacitance

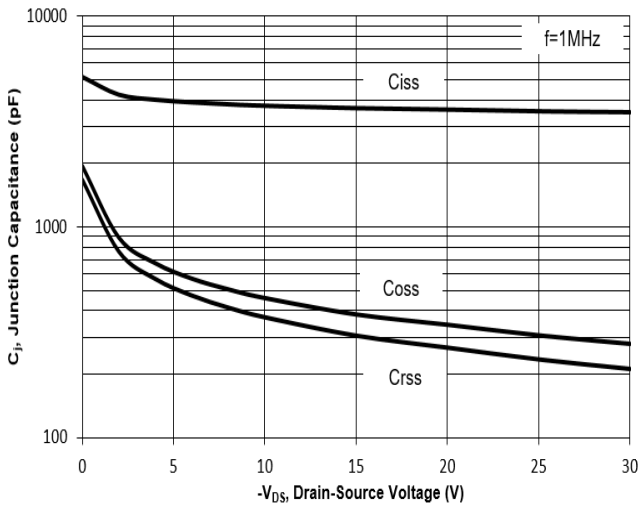


Fig. 8 Drain-Source Leakage Current vs. T_j

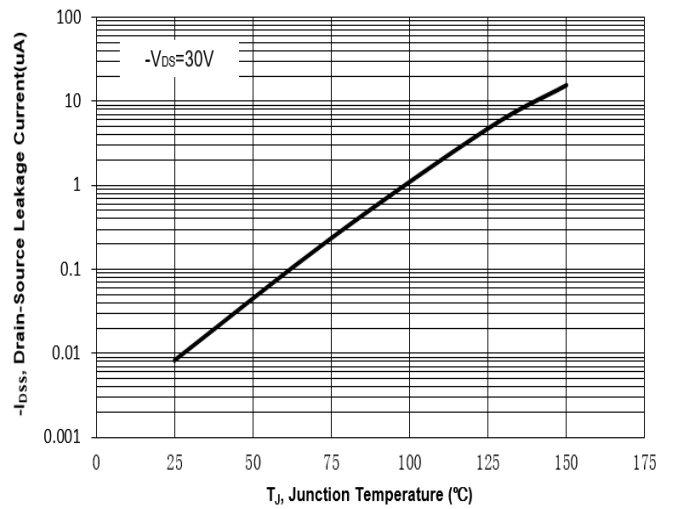


Fig. 9 $V_{(BR)DSS}$ vs. Junction Temperature

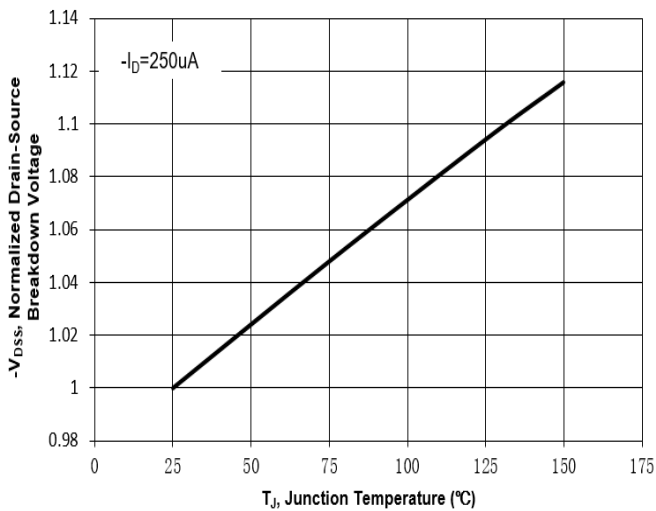


Fig. 10 Gate Threshold Variation vs. T_j

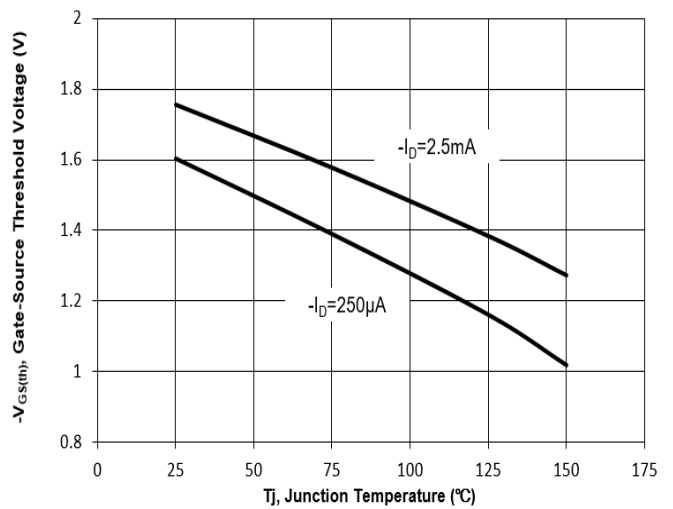


Fig. 11 Gate Charge

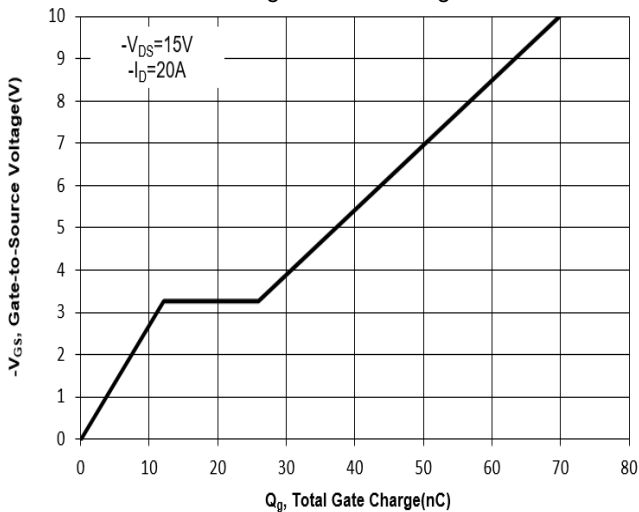
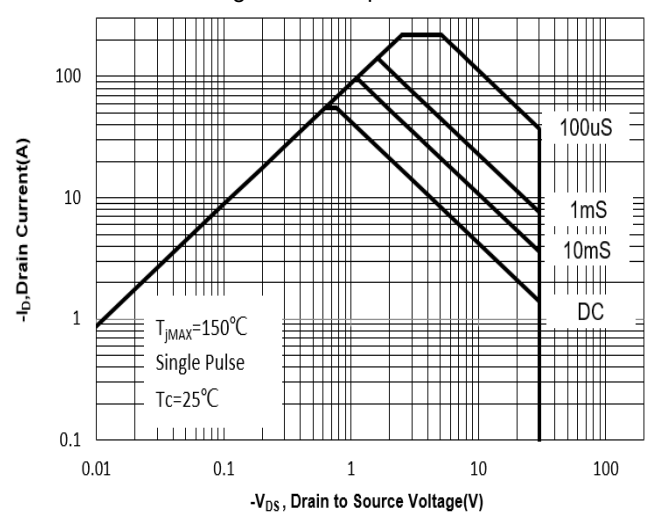


Fig. 12 Safe Operation Area



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Electrical Characteristics Curves

Fig. 13 Normalized Maximum Transient Thermal Impedance($Z_{\theta JC}$)

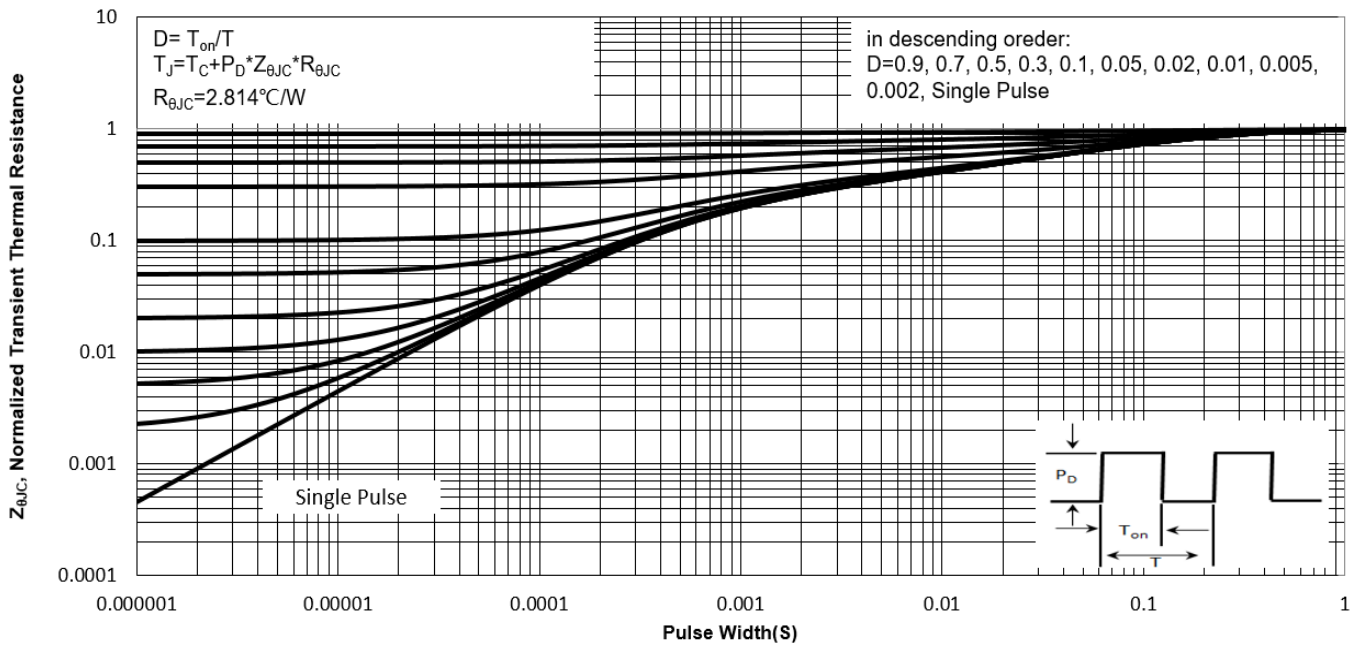
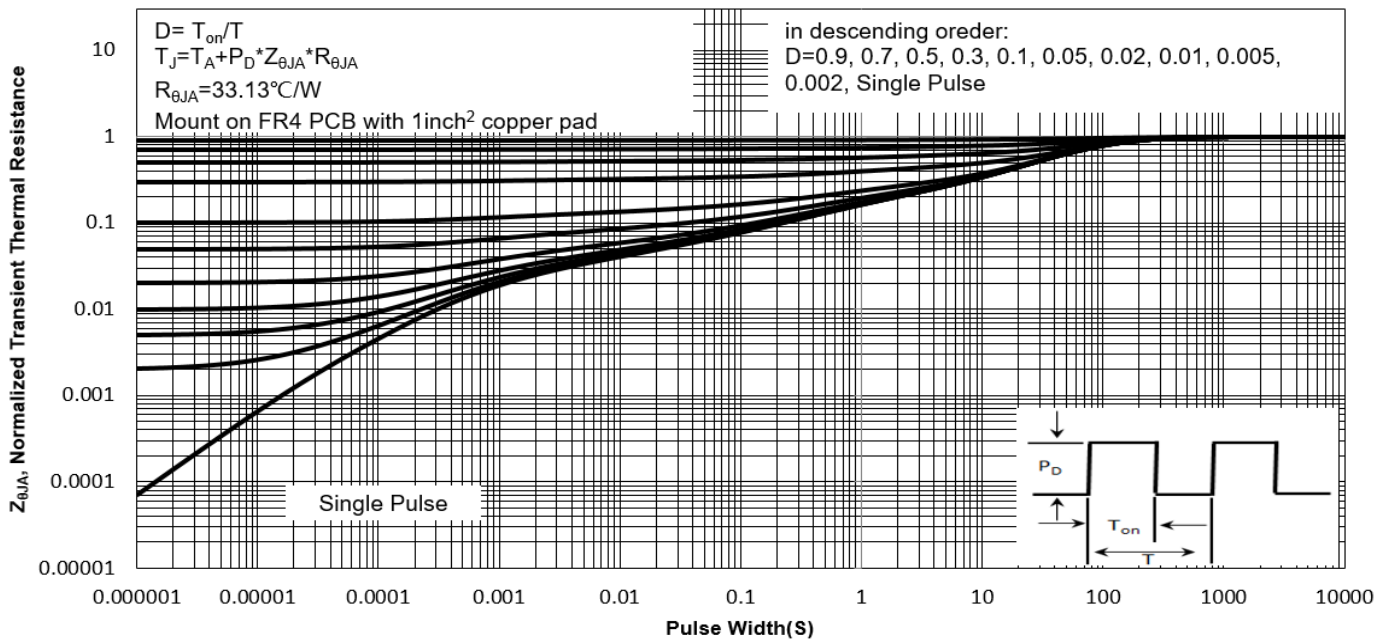


Fig. 14 Normalized Maximum Transient Thermal Impedance($Z_{\theta JA}$)



Test Circuits

Fig.1-1 Switching times test circuit

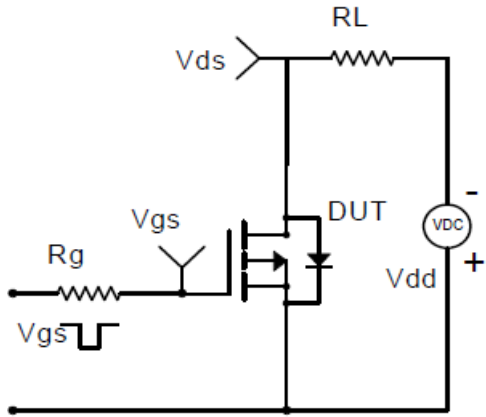


Fig.1-2 Switching Waveform

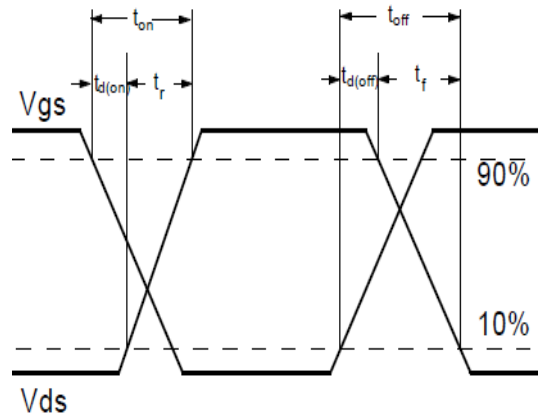


Fig.2-1 Gate charge test circuit

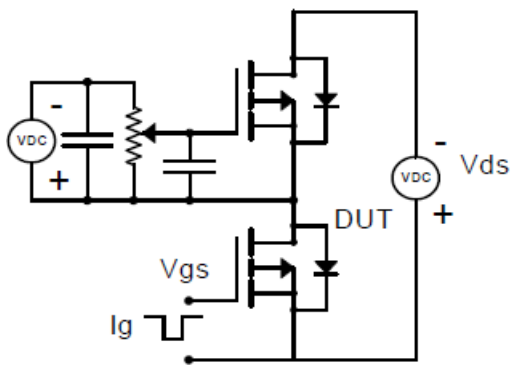


Fig.2-2 Gate charge waveform

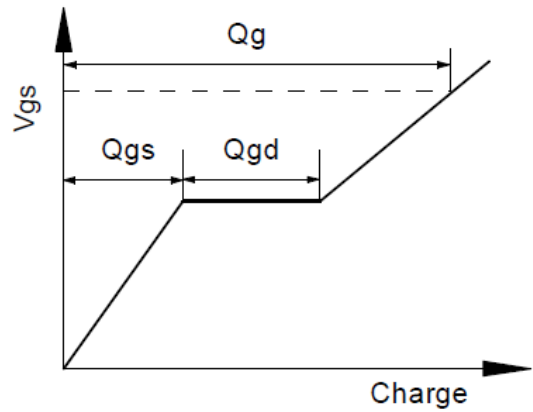


Fig.3-1 Avalanche test circuit

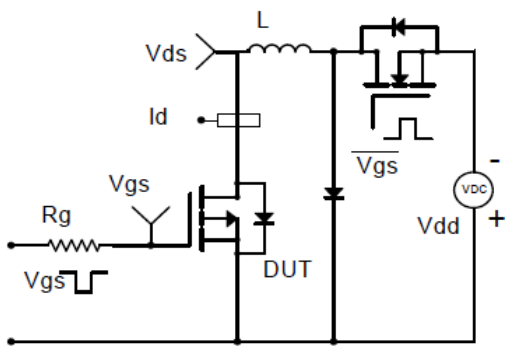
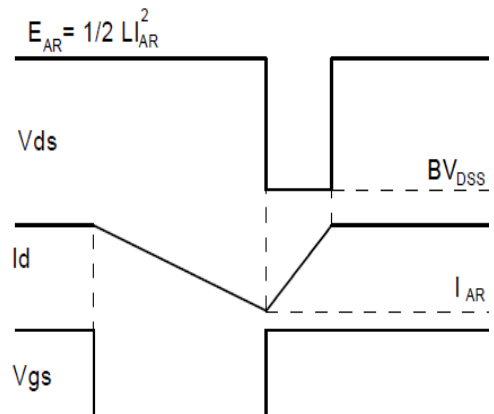


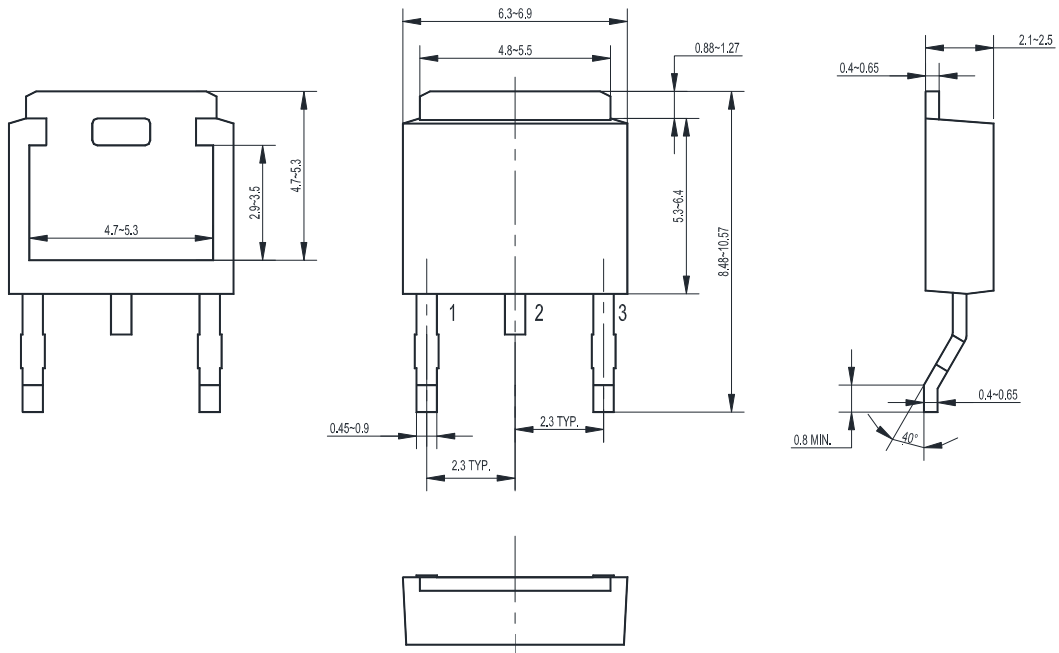
Fig.3-2 Avalanche waveform



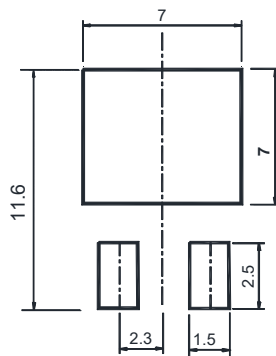
WTR03P080LS-HAF

Package Outline (Dimensions in mm)

TO-252



Recommended Soldering Footprint



Packing information

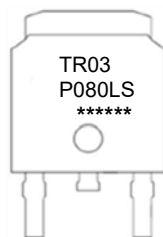
Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
TO-252	12	8 ± 0.1	0.315 ± 0.004	330	13	2,500

Marking information

" TR03P080LS " = Part No.

" ***** " = Date Code Marking

Font type: Arial



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